

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Christoph A. Wasshuber

Art Unit:

2818

Serial No.:

10/714,366

Examiner:

Nguyen, T.

Filing Date:

11/13/2003

Docket No.:

TI-36280

Customer No.: 23494

Conf. No.:

1864

Title: FORMING A SEMICONDUCTOR STRUCTURE IN MANUFACTURING A SEMICONDUCTOR DEVICE USING ONE OR MORE EPITAXIAL GROWTH

PROCESSES

ELECTION

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(A)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as first Class Mail in an envelope addressed to Commissioner for Patents, Alexandria, VA 22313-1450.

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Marianna Smith

Dear Sir:

With respect to the Restriction Requirement mailed on 02/07/2005, the Examiner has restricted the instant application to the invention of Group I (Claims 1-11), or Group II (Claims 12-20). In light of this, Applicant elects to pursue Group I, (Claims 1-11) without traverse.

Respectfully submitted,

lacqueline J. Garner Attorney for Applicant Reg. No. 36,144

Texas Instruments Incorporated P.O. Box 655474, M/S 3999 Dallas, TX 75265 (214) 532-9348 or (972) 917-5643 Fax: (972) 917-4418